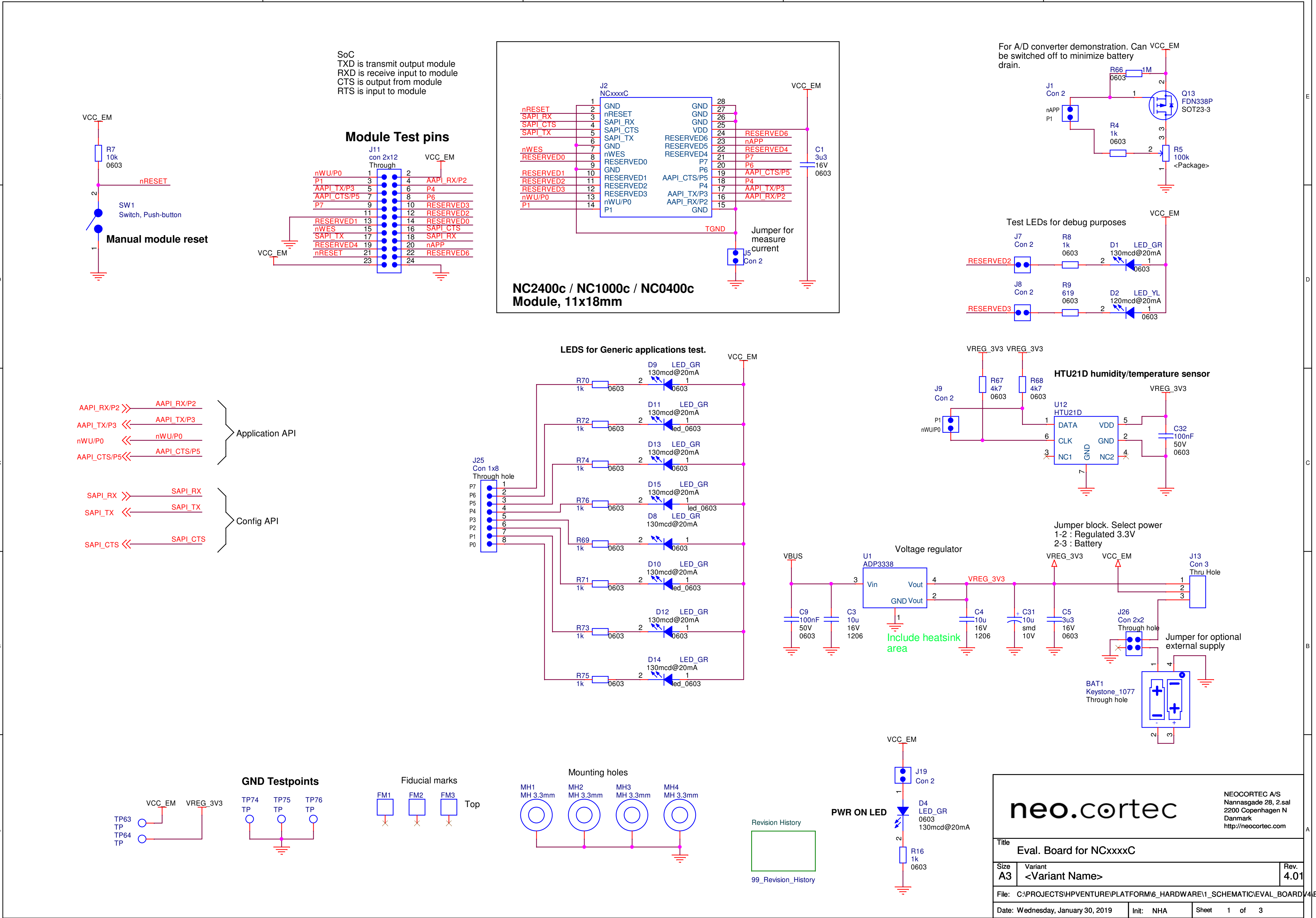
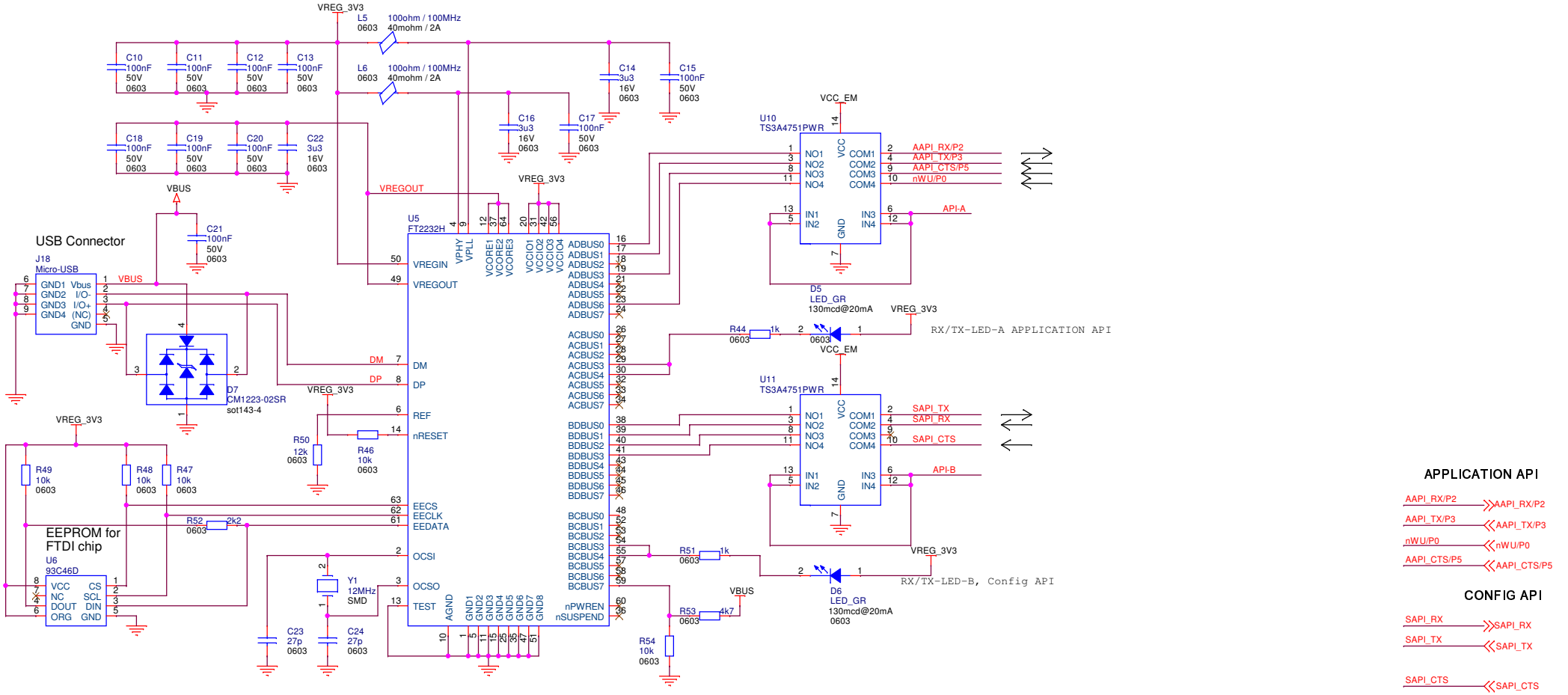

SoC
TXD is transmit output module
RXD is receive input to module
CTS is output from module
RTS is input to module
Module Test pins
J11
con 2x12
Through
VCC_EM
nWU/P0
P1
AAPI_TX/P3
AAPI_CTS/P5
P7
RESERVED1
nWES
SAPI_TX
RESERVED4
nRESET
AAPI_RX/P2
P4
P6
RESERVED3
RESERVED2
RESERVED0
SAPI_CTS
SAPI_RX
nAPP
RESERVED6
R7
10k
0603
SW1
Switch, Push-button
Manual module reset
J2
NCxxxxC
GND
nRESET
SAPI_RX
SAPI_CTS
SAPI_TX
nWES
RESERVED0
RESERVED1
RESERVED2
RESERVED3
nWU/P0
VDD
RESERVED5
AAPI_TX/P3
TGND
Jumper for measure current
J5
Con 2
C1
3u3
16V
0603
NC2400c / NC1000c / NC0400c Module, 11x18mm
For A/D converter demonstration. Can be switched off to minimize battery drain.
R66
1M
J1
Con 2
Q13
FDN338P
SOT23-3
R4
1k
R5
100k
<Package>
Test LEDs for debug purposes
J7
R8
D1
LED_GR
130mcd@20mA
J8
R9
619
D2
LED_YL
120mcd@20mA
LEDS for Generic applications test.
J25
Con 1x8
Through hole
R70
R72
R74
R76
R69
R71
R73
R75
D9
D11
D13
D15
D8
D10
D12
D14
led_0603
Application API
Config API
VREG_3V3
HTU21D humidity/temperature sensor
J9
R67
4k7
R68
U12
HTU21D
DATA
CLK
NC1
NC2
C32
100nF
50V
Jumper block. Select power
1-2 : Regulated 3.3V
2-3 : Battery
Voltage regulator
VBUS
U1
ADP3338
Vin
Vout
GND Vout
C9
C3
10u
1206
C4
C31
smd
10V
C5
Include heatsink area
J13
Con 3
Thru Hole
J26
Con 2x2
Jumper for optional external supply
BAT1
Keystone_1077
PWR ON LED
J19
D4
R16
GND Testpoints
TP63
TP64
TP74
TP75
TP76
TP
Fiducial marks
FM1
FM2
FM3
Top
Mounting holes
MH1
MH2
MH3
MH4
MH 3.3mm
Revision History
99_Revision_History
neo.cortec
NEOCORTEC A/S
Nannasgade 28, 2.sal
2200 Copenhagen N
Danmark
http://neocortec.com
Title
Eval. Board for NCxxxxC
Size
A3
Variant
<Variant Name>
Rev.
4.01
File: C:\PROJECTS\HPVENTURE\PLATFORM\6_HARDWARE\1_SCHEMATIC\EVAL_BOARDV4\EVA
Date: Wednesday, January 30, 2019
Init: NHA
Sheet 1 of 3



Test Point is 0.8mm through hole, plated

neo.cortec

NEOCORTEC A/S
Nannasgade 28, 2.sal
2200 Copenhagen N
Danmark
http://neocortec.com

Title: Eval. Board for NCxxxxC		
Size: A3	Variant: <Variant Name>	Rev.: 4.01
File: C:\PROJECTS\HPVENTURE\PLATFORM\6_HARDWARE\1_SCHEMATIC\EVAL_BOARDV4\EVA		
Date: Wednesday, January 30, 2019	Init: NHA	Sheet 2 of 3